

(19) Europäisches Patentamt

European Patent Office

Office européen des brevets



(11) EP 1 482 550 A2

(12)

## EUROPEAN PATENT APPLICATION

(43) Date of publication:  
01.12.2004 Bulletin 2004/49

(51) Int Cl.7: H01L 21/762, H01L 21/316

(21) Application number: 04012238.4

(22) Date of filing: 24.05.2004

(84) Designated Contracting States:  
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR  
HU IE IT LI LU MC NL PL PT RO SE SI SK TR  
Designated Extension States:  
AL HR LT LV MK

(30) Priority: 30.05.2003 JP 2003154429

(71) Applicants:  
• National Institute of Advanced Industrial  
Science and Technology  
Tokyo 100-8921 (JP)

• Semiconductor Process Laboratory Co., Ltd.  
Ichikawa-shi, Chiba 272-0001 (JP)

(72) Inventors:  
• Ohdaira, Toshiyuki  
1-1, Umezono 1-chome Tsukuba-shi Ibaraki (JP)  
• Shioya, Yoshimi  
717-30, Futamata Ichikawa-shi Chiba (JP)

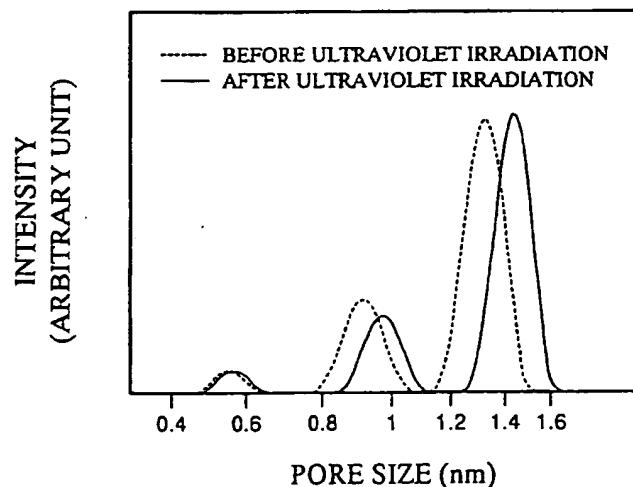
(74) Representative: Schwabe - Sandmair - Marx  
Stuntzstrasse 16  
81677 München (DE)

### (54) Low dielectric constant insulating film and method of forming the same

(57) The present invention relates to a method of forming a low dielectric constant insulating film. Its constitution has the steps of: (a) forming an insulating film containing Si-CH<sub>3</sub> bond in the skeleton of Si-O-Si on a

substrate; (b) irradiating ultraviolet ray to the insulating film in reduced-pressure atmosphere to break CH<sub>3</sub> groups from Si-CH<sub>3</sub> bond in the insulating film; and (c) ejecting the broken CH<sub>3</sub> groups from the insulating film.

FIG. 2



**Description**

**BACKGROUND OF THE INVENTION**

5      1. Field of the Invention

[0001] The present invention relates to a low dielectric constant insulating film and a method of forming the same.

10     2. Description of the Prior Art

[0002] In recent years, an insulating film having a low dielectric constant (hereinafter, referred to as a low dielectric constant insulating film) is used in a semiconductor integrated circuit in order to suppress the delay of signals communicating in wirings and to improve the processing speed of the entire circuit.

[0003] Although a semiconductor roadmap requires an interlayer insulating film having a relative dielectric constant of 2.5 or less in and after a generation when a design rule becomes 65nm, it has turned out after studying various insulating materials that it is difficult to realize the relative dielectric constant of 2.5 or less with raw (non-porous) material. For this reason, there has been used a way such as reducing the effective dielectric constant of the entire insulating film by reducing a film density while employing an insulating material having the relative dielectric constant of 2.5 or more. It is a method such that pores of nanometers to sub-nanometers are introduced in a formed insulating film to make it porous and thus reduce a film density.

[0004] For example, Patent Document 1 describes an example to make it porous by introducing dopant into the formed film and then removing it from the film by oxidation or the like.

[Patent Document 1]

25     Japanese Patent Laid-open 2000-273176 publication

[0005] However, when the pores are introduced in the insulating film to make it porous, there occurs a problem such that mechanical strength of the entire film is reduced significantly and thus the film cannot withstand a polishing process (CMP: chemical mechanical polishing) performed for planarization in process after deposition. To solve the problem, a pore size is made smaller or the porosity is reduced. With this, although the mechanical strength of the film is improved, a low relative dielectric constant so as to be required is not obtained.

**SUMMARY OF THE INVENTION**

35     [0006] The object of the present invention is to provide a low dielectric constant insulating film and method of forming the same capable of forming an insulating film, which has high film strength adapted for existing conventional semiconductor manufacturing process and a lower relative dielectric constant.

[0007] According to the present invention, first, an existing film-forming method such as a plasma-enhanced chemical vapor deposition method and a spin coating method is used to form on a substrate an insulating film that is non-porous or porous such as being as small as a few nanometers or less in pore size, and has Si-CH<sub>3</sub> bond with respect to the skeleton of Si-O-Si which has sufficient mechanical strength. Alternatively, it's method is used to form an insulating film that is non-porous or porous and as small as a few nanometers or less in pore size, and has Si-CH<sub>3</sub> bond with respect to the silica skeleton of Si-O other than Si-O-Si, and the insulating film whose mechanical strength can be improved by ultraviolet irradiation in reduced-pressure atmosphere after the film formation.

[0008] Subsequently, CH<sub>3</sub> groups are broken from Si-CH<sub>3</sub> bond in the insulating film by the ultraviolet irradiation in the reduced-pressure atmosphere or in the reduced-pressure atmosphere mainly containing inert gas and nitrogen, and then the broken CH<sub>3</sub> groups are ejected from the insulating film. For example, it is possible to facilitate ejecting of the broken CH<sub>3</sub> groups by simultaneously heating the substrate during the ultraviolet irradiation. Thus, in the case of the non-porous insulating film, the film is made porous, or in the case of the porous insulating film, small pores in the film are made larger.

[0009] In this case, the energy of ultraviolet to be irradiated is set higher than the binding energy of Si-CH<sub>3</sub> bond groups and lower than the binding energy of Si-O-Si or Si-O other than Si-O-Si, which forms the skeleton. For example, by setting the wavelength of ultraviolet to 120nm (equivalent to 10eV of energy) or more, the CH<sub>3</sub> groups can be broken from Si-CH<sub>3</sub> bond in the insulating film without affecting the skeleton of Si-O-Si or the like. In other words, it is important in the present invention to form an insulating film originally being solid in a skeleton or an insulating film that can be transformed into an insulating film being solid in a skeleton by ultraviolet irradiation in the reduced-pressure atmosphere in order to improve the film strength. Particularly, when using an organic compound having Si-CH<sub>3</sub> bond and siloxane

bond as a deposition material, the film strength depends on Si-O-Si bond that serves as a skeleton, and it is preferable in comparison to other skeletons. Alternatively, when using another organic compound having Si-CH<sub>3</sub> bond, it is transformed into the insulating film being solid in a skeleton by ultraviolet irradiation in the reduced-pressure atmosphere, and thus it is preferable.

5 [0010] According to the present invention, an initial film formation is performed for the insulating film being originally solid in a skeleton of Si-O-Si, or for the insulating film, which has the silica skeleton other than Si-O-Si and can be transformed into the insulating film being solid in a skeleton by ultraviolet irradiation in the reduced-pressure atmosphere. Then by irradiating ultraviolet onto the insulating film in the reduced-pressure atmosphere instead of oxidation, CH<sub>n</sub> groups, which are organic groups, are eliminated and ejected from the insulating film without affecting the skeleton.

10 Therefore, It is possible to improve the strength of the insulating film and allow the insulating film to have a lower dielectric constant.

#### BRIEF DESCRIPTION OF THE DRAWINGS

15 [0011]

Fig. 1 is a side view showing the constitution of an ultraviolet irradiation processing apparatus used in the method of forming the low dielectric constant insulating film, which is an embodiment of the present invention.

20 Fig. 2 is a graph showing the changes of a pore size distribution before and after ultraviolet irradiation processing in a plasma CVD insulating film formed by the method of forming the low dielectric constant insulating film, which is a first example of the present invention.

Fig. 3 is a graph showing the status of pore size before and after ultraviolet irradiation processing in the plasma CVD insulating film formed by the method of forming the low dielectric constant insulating film, which is the first example of the present invention.

25 Fig. 4 is a graph showing the degree of ejection of CH<sub>3</sub> groups before and after ultraviolet irradiation processing in the plasma CVD insulating film formed by the method of forming the low dielectric constant insulating film, which is the first example of the present invention.

Fig. 5 is a graph showing the mechanical strength (Young's modulus) before and after ultraviolet irradiation processing in the plasma CVD insulating film formed by the method of forming the low dielectric constant insulating film, which is the first example of the present invention.

30 Fig. 6 is a graph showing the mechanical strength (hardness) before and after ultraviolet irradiation processing in the plasma CVD insulating film formed by the method of forming the low dielectric constant insulating film, which is the first example of the present invention.

Fig. 7 is a graph showing the changes of the pore size before and after ultraviolet irradiation processing in a coated insulating film formed by the method of forming the low dielectric constant insulating film, which is a fourth example of the present invention.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

40 [0012] Embodiments of the present invention will be explained with reference to the drawings hereinafter.

(Explanation of the ultraviolet processing apparatus used in the method of forming the low dielectric constant insulating film, which is the embodiment of the present invention)

45 [0013] Fig. 1 is the side view showing the constitution of the ultraviolet processing apparatus used in the method of forming the low dielectric constant insulating film according to the embodiment of the present invention.

[0014] The ultraviolet processing apparatus comprises a chamber 1 capable of being reduced in pressure, and the chamber 1 is connected to a vacuum-pumping unit 2 through an exhaust pipe 4, as shown in Fig. 1. An open/close valve 5 that controls evacuation/no-evacuation of the chamber 1 is provided halfway the exhaust pipe 4.

50 [0015] A substrate-holding stage 8 is provided in the chamber 1, and the substrate-holding stage 8 is provided with a heater 9 that heats a substrate 10 on the substrate-holding stage 8.

[0016] Further, an ultraviolet light source 3 is provided above the substrate-holding stage 8 of the chamber 1 via an ultraviolet ray transmission path 6. A shutter 7 that controls open/close of the path of ultraviolet ray is provided halfway the ultraviolet ray transmission path 6.

55 [0017] A deuterium lamp, a low-pressure mercury lamp, a xenon lamp or the like can be used as the ultraviolet light source 3. Since the ultraviolet ray generated from the lamp is not monochromatic and its energy distributes over a wide range, there is a possibility that the skeleton bond in the insulating film is broken. In this case, it is desirable to irradiate ultraviolet ray via a filter that cuts high-energy ultraviolet ray breaking the skeleton bond in the insulating film. Note that

laser (such as KrF laser, ArF laser and F<sub>2</sub> laser), various excimer lamps or the like may be used as long as it matches a range of energy capable of eliminating CH<sub>n</sub> groups from Si-CH<sub>n</sub> (n=1, 2, 3) bond without affecting the skeleton in the insulating film.

[0018] Note that the above-described ultraviolet processing apparatus is an apparatus that performs ultraviolet irradiation processing independently, but a supply source of deposition gas and an introduction port of deposition gas for the chamber may be provided, electrodes opposed to each other (opposing electrodes) may be provided in the chamber, and a high-frequency power source may be connected to the opposing electrodes in order to enable the apparatus to perform deposition by a plasma enhanced CVD method. This makes it possible for the apparatus to perform deposition and ultraviolet irradiation processing after deposition continuously in a same chamber.

[0019] Further, in the case of a plasma enhanced CVD apparatus adopting a multi-chamber system, one chamber is used for ultraviolet processing. Thus, a barrier film or the like can be formed without exposing the substrate 10 to atmosphere after ultraviolet processing. With this, it is possible to prevent the increase of the relative dielectric constant or the deterioration of capability of breakdown voltage due to moisture adsorption or the like.

[0020] (Explanation of the method of forming the low dielectric constant insulating film, which is the embodiment of the present invention)

[0021] Firstly, the entire process for forming the low dielectric constant insulating film will be explained.

[0022] The substrate is subject thereon to formation of an insulating film, where contains Si-CH<sub>n</sub> (n=1, 2, 3) bond to Si-O-Si or another silica skeleton. In this case, there exist the following two methods as the film-forming method.

(a) Using a parallel plate plasma enhanced CVD apparatus, deposition gas containing organic compound having siloxane bond or another organic compound, which has Si-CH<sub>3</sub> bond, is introduced between the opposing electrodes. Electric power is applied between the opposing electrodes to generate plasma to cause reaction. Thus a CVD insulating film containing Si-CH<sub>n</sub> bond is formed on the substrate.

(b) Organic SOG having Si-CH<sub>3</sub> bond and siloxane bond is coated on the substrate by a spin coating method, and then the coated film that has been formed is heated to evaporate solvent. Thus a coated insulating film containing Si-CH<sub>n</sub> bond is formed.

[0023] Next, ultraviolet ray is irradiated on the formed insulating film in the reduced-pressure atmosphere to break CH<sub>n</sub> groups from Si-CH<sub>n</sub> bond in the insulating film. In this case, the wavelength of ultraviolet ray is set to 120nm or more. The wavelength is equivalent to the energy of 10eV or less, which matches the range of energy capable of eliminating CH<sub>n</sub> groups from Si-CH<sub>n</sub> bond without affecting the skeleton of Si-O-Si or the like.

[0024] Then, the broken CH<sub>n</sub> groups are ejected from the insulating film. For example, substrate-heating temperature is set to room temperature to 450°C, preferably to 100°C to 450°C. As a result, the broken CH<sub>3</sub> groups are ejected from the insulating film. Thus, the low dielectric constant insulating film is formed.

[0025] Note that the reason why the upper limit of the substrate heating temperature is set to 450°C is to prevent alteration of material itself or reaction with surrounding matter when copper or aluminum has already been formed. Further, it is because although room temperature or more is acceptable as the lower limit of the heating temperature, CH<sub>n</sub> groups can be ejected faster when the heating temperature is set to 100°C or more.

[0026] In a series of the above-described steps, the step of irradiating ultraviolet ray to break CH<sub>3</sub> groups from Si-CH<sub>3</sub> bond in the insulating film and the step of ejecting the broken CH<sub>3</sub> groups from the insulating film can be performed simultaneously. In this case, ultraviolet ray is irradiated while heating the substrate.

[0027] Note that a series of the above-described steps may be repeated to stack the low dielectric constant insulating films of this embodiment in plural layers, and thus a thick low dielectric constant insulating film can be formed as an entire body.

[0028] Next, particularly effective combination of a specific deposition material will be cited.

(i) Deposition gas

(α) Constitution of deposition gas

- (a) Oxidizing gas / organic compound having siloxane bond and Si-CH<sub>3</sub> bond
- (b) Oxidizing gas / organic compound having siloxane bond and Si-CH<sub>3</sub> bond / C<sub>x</sub>H<sub>y</sub>F<sub>z</sub>
- (c) Oxidizing gas / another organic compound having Si-CH<sub>3</sub> bond
- (d) Oxidizing gas / another organic compound having Si-CH<sub>3</sub> bond / C<sub>x</sub>H<sub>y</sub>F<sub>z</sub>

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(B) Specific examples of the organic compound having siloxane bond and Si-CH<sub>3</sub> bond, the oxidizing gas and C<sub>x</sub>H<sub>y</sub>F<sub>z</sub>

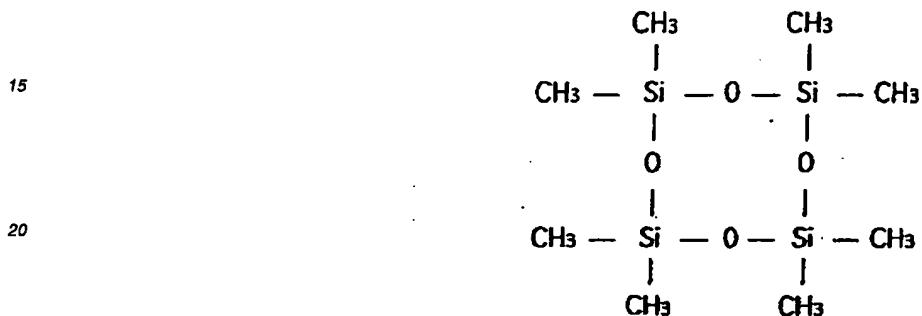
(a) Organic compound having siloxane bond and Si-CH<sub>3</sub> bond

5 Hexamethyldisiloxane

(HMDSO: (CH<sub>3</sub>)<sub>3</sub>Si-O-Si(CH<sub>3</sub>)<sub>3</sub>)

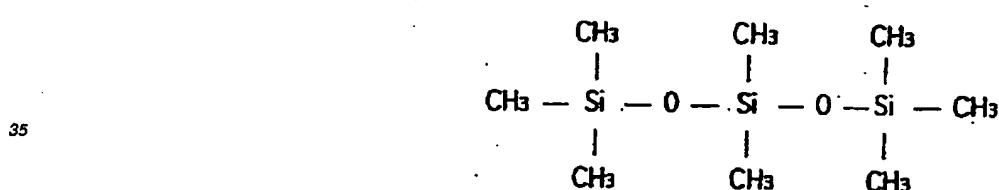
Octamethylcyclotetrasiloxane (OMCTS)

10 [formula 1]



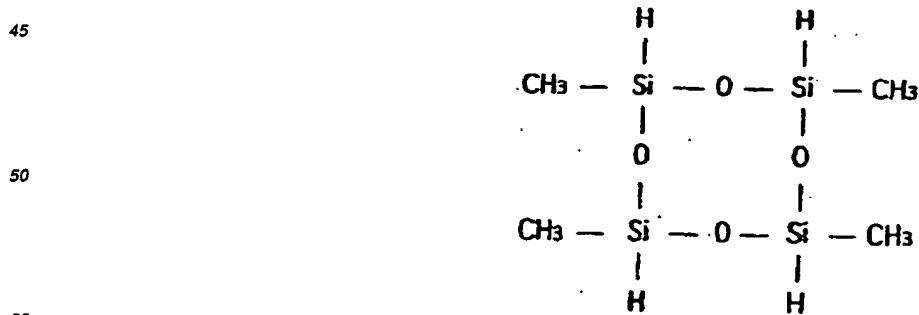
25 Octamethyltrisiloxane (OMTS)

30 [formula 2]



40 Tetramethylcyclotetrasiloxane (TMCTS)

[formula 3]



55 (b) Another organic compound having Si-CH<sub>3</sub> bond  
Dimethyldimethoxysilane (Si(CH<sub>3</sub>)<sub>2</sub>OCH<sub>3</sub>)<sub>2</sub>)

5            Tetramethylsilane ( $\text{Si}(\text{CH}_3)_4$ )  
           Trimethylsilane ( $\text{SiH}(\text{CH}_3)_3$ )  
           Dimethylsilane ( $\text{SiH}_2(\text{CH}_3)_2$ )  
           Monomethylsilane ( $\text{SiH}_3\text{CH}_3$ )  
           (c) Oxidizing gas  
            $\text{H}_2\text{O}$ ,  $\text{N}_2\text{O}$ ,  $\text{CH}_3\text{OH}$ ,  $\text{C}_2\text{H}_5\text{OH}$ ,  $\text{H}_2\text{O}_2$  or  $\text{O}_2$   
           One of the oxidizing gas or combination of two or more of the oxidizing gas can be used.  
           (d)  $\text{C}_x\text{H}_y\text{F}_z$  (x is a positive integer, y is 0 or a positive integer, z is 0 or a positive integer, and y and z do not become 0 at the same time.)

10             $\text{C}_3\text{F}_8$   
            $\text{C}_4\text{F}_8$   
            $\text{CHF}_3$   
            $\text{C}_2\text{H}_4$

15            (ii) Coating solution  
           (α) Organic SOG having siloxane bond and  $\text{Si}-\text{CH}_3$  bond  
           Alkylsilsesquioxane polymer (MSQ)

20            [0029] Next, regarding the low dielectric constant insulating film formed by the above-described method of forming low dielectric constant insulating film, explanation will be given for the result of investigation performed for the aspect of changes of pore size in the film before and after the ultraviolet irradiation processing, the state of pore distribution in the film before and after the ultraviolet irradiation processing, the degree of ejection of  $\text{CH}_3$  groups in the film before and after the ultraviolet irradiation processing, and the aspect of changes of mechanical strength and the relative dielectric constant of the film before and after the ultraviolet irradiation processing.

25            (1) First Example

30            [0030] A sample is obtained as follows. A silicon oxide film is formed on a silicon substrate based on the following film-forming conditions of plasma enhanced CVD, followed by performing the ultraviolet irradiation processing on the following ultraviolet processing conditions. Note that a silicon oxide film is an insulating film containing Si, O, C and H, hereinafter.

35            (Film-forming conditions I)  
           [0031]  
           (i) Deposition gas conditions  
           HMDSO gas flow rate: 50 sccm  
           40             $\text{H}_2\text{O}$  gas flow rate: 1000 sccm  
            $\text{C}_4\text{F}_8$  gas flow rate: 50 sccm  
           Gas pressure: 1.75 Torr  
           (ii) Conditions for generating plasma  
           High-frequency power (13.56 MHz) PHF: 300W  
           45            Low-frequency power (380 KHz) PLF: 0W  
           (iii) Substrate heating temperature: 375°C  
           (iv) Deposited silicon oxide film  
           Film thickness: 650nm

50            (Ultraviolet processing conditions)  
           [0032]  
           (i) Ultraviolet light source: Deuterium lamp  
           55            Ultraviolet ray wavelength: 120nm to 400nm  
           Power: 30W  
           (ii) Substrate heating: 400°C  
           (iii) Processing time: 30 minutes

[0033] Next, regarding the low dielectric constant insulating film formed as described above, the investigation is performed for the aspect of changes of the pore size in the film before and after the ultraviolet irradiation processing. The investigation is conducted by measuring the pore size distribution using a positron-annihilation lifetime spectroscopy developed by National Institute of Advanced Industrial Science and Technology (AIST) who is the applicant of the present invention. The positron-annihilation lifetime spectroscopy is a method of measuring the size and the distribution of pores by irradiating positrons on the sample and detecting  $\gamma$  ray generated from the inside of the sample by a scintillator and a photomultiplier.

[0034] Fig. 2 shows the result. The axis of ordinate in Fig. 2 shows relative strength expressed in linear scale and the axis of abscissa in Fig. 2 shows the pore size distributions (nm). In Fig. 2, the dotted line and the solid line show pore size distributions before and after the ultraviolet irradiation processing, respectively.

[0035] According to Fig. 2, the pore size has a wide distribution, and the entire distribution of the pore size shifts toward a larger size by the ultraviolet irradiation. It shows that the pore size becomes larger. The average pore size is 1.22nm before the ultraviolet irradiation processing and becomes 1.36nm after the ultraviolet irradiation processing.

[0036] Next, the investigation is performed for the distribution of the average pore size in a film thickness direction of the formed film before and after the ultraviolet irradiation processing. Fig. 3 shows the result. The axis of ordinate and the axis of abscissa of Fig. 3 respectively show the pore size (nm) and the depth (nm) expressed in logarithmic scale measured from the surface of the formed film. In Fig. 3, the solid line connecting white circles shows the average pore size before the ultraviolet irradiation processing, and the solid line connecting black circles shows the average pore size after the ultraviolet irradiation processing.

[0037] According to Fig. 3, it turns out that the changes of the pore size by the ultraviolet irradiation processing occur in the entire film thickness of the formed film.

[0038] Next, the investigation is performed for the degree of ejection of methyl groups ( $\text{CH}_3$  groups) from the formed film before and after the ultraviolet irradiation processing. The investigation is conducted by measuring infrared absorption spectrum of the formed film. Fig. 4 shows the result. The axis of ordinate and the axis of abscissa respectively show absorption intensity (arbitrary unit) expressed in linear scale and wavenumber ( $\text{cm}^{-1}$ ).

[0039] According to the result shown in Fig. 4, the ultraviolet irradiation significantly reduces signal intensity from  $\text{Si}-\text{CH}_3$  bond (wavenumber:  $1275\text{cm}^{-1}$ ) and C-H bond (wavenumber:  $2950\text{cm}^{-1}$ ). It is presumed from the result that the expansion of the pore size by the ultraviolet irradiation is caused by elimination of methyl groups in the pore. On the other hand, since the peak shape of Si-O bond distributed in the wavenumber from  $1000\text{cm}^{-1}$  to  $1250\text{cm}^{-1}$  changes little, it is considered that the silica skeleton made up of Si-O-Si bond does not change much.

[0040] Note that it is confirmed that no change occurs at all in the pore size distribution and the infrared absorption spectrum when simply the heating processing of  $400^\circ\text{C}$  is performed for the same period of time without applying the ultraviolet irradiation.

[0041] Next, the investigation is performed for the mechanical strength (Young's modulus and hardness) of the formed film before and after the ultraviolet irradiation. The investigation is conducted by gradually allowing a probe to penetrate into the inside of the sample from the surface thereof and measuring Young's modulus and the like at an arbitrary depth.

[0042] Fig. 5 and Fig. 6 show the results. The axis of ordinate and the axis of abscissa of Fig. 5 respectively show Young's modulus (GPa) expressed in linear scale and the depth (nm) from the surface expressed in linear scale. The axis of ordinate and the axis of abscissa of Fig. 6 respectively show hardness (GPa) expressed in linear scale and the depth (nm) from the surface expressed in linear scale.

[0043] According to the investigation results, when the values at the depth of 30nm to 40nm from the surface are regarded as Young's modulus and hardness, Young's modulus of 12.73 GPa and the hardness of 1.87 GPa before the ultraviolet irradiation become Young's modulus of 23.98 GPa and the hardness of 3.01 GPa after the ultraviolet irradiation. The mechanical strength of film is not only maintained but also improved by the ultraviolet irradiation. As the reason, it is considered that methyl groups is eliminated due to the ultraviolet irradiation and broken bonds (dangling bonds) partially combine each other, so that the strength of the skeleton increases.

[0044] Next, the investigation is performed for the relative dielectric constant of the film before and after the ultraviolet irradiation processing. The investigation is conducted as follows. That is, applying direct current voltage to a silicon oxide film, capacitance is measured by a C-V measurement method where a signal of 1MHz is superposed to the direct current voltage, and then the constant is calculated from a capacitance value. According to the investigation result, the relative dielectric constant of 2.74 before the ultraviolet irradiation becomes 2.67 after the ultraviolet irradiation.

[0045] As described above, it is possible to maintain or improve the film strength and reduce the relative dielectric constant by the ultraviolet irradiation. Consequently, the method of forming the low dielectric constant insulating film according to the present invention is extremely effective.

[0046] Note that the improvement of film strength is recognized. It is presumed to be caused by the recombination of the dangling bonds where methyl groups are eliminated. However, if such recombination reaction occurs too much, shrinkage and high density of the film occur in some cases and thus there is a possibility that the relative dielectric constant increases. Further, since methyl groups have a function to improve moisture resistance, removing all methyl

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groups is not necessarily good for the low dielectric constant insulating film. Therefore, it is necessary to adjust the degree of the recombination reaction occurrence and the amount of methyl groups to be removed. The adjustment can be performed by adjusting ultraviolet irradiation dose (such as power and irradiation time).

5 (2) Second Example

[0047] In the second example, a silicon oxide film is formed using deposition gas different from that of the first example, and investigation is conducted as to how the difference of the substrate-heating temperature during the ultraviolet irradiation affects the pore size and the relative dielectric constant. The silicon oxide film as the low dielectric constant insulating film is formed on the silicon substrate in the same manner as the first example, and then the ultraviolet irradiation is performed to the silicon oxide film. A sample is obtained in this manner. The silicon oxide film is formed under the following film-forming conditions by the plasma CVD method.

(Film-forming conditions II)

15

[0048]

- (i) Deposition conditions
  - HMSO gas flow rate: 50 SCCM
  - H<sub>2</sub>O gas flow rate: 1000 SCCM
  - Gas pressure: 1.75 Torr
- (ii) Conditions for generating plasma
  - High-frequency power (13.56 MHz) PHF: 300W
  - Low-frequency power (380 KHz) PLF: 0W
- (iii) Substrate heating temperature: 375°C
- (iv) Deposited silicon oxide film
  - Film thickness: 650nm

30

(Ultraviolet processing conditions)

35

[0049]

- (i) Ultraviolet light source: Deuterium lamp
  - Ultraviolet ray wavelength: 120nm to 400nm
  - Power: 30W
- (ii) Substrate heating: 200°C, 400°C
- (iii) Processing time: 20 minutes

[0050] Regarding the silicon oxide film, the investigation is performed for the changes of the pore size in the film and the relative dielectric constant ( $\kappa$ ) of the film before and after the ultraviolet irradiation processing.

[0051] According to the investigation results, the pore size, which is 0.96nm before the ultraviolet irradiation, becomes 1.02nm at the substrate heating temperature of 200°C and 1.17nm at 400°C after the ultraviolet irradiation.

[0052] Note that the relative dielectric constant, which is approximately 2.58 before the ultraviolet irradiation, reduces to 2.42 after the ultraviolet irradiation.

[0053] From the above-described investigation results, a larger pore size is obtained and a lower relative dielectric constant is expected when the substrate-heating temperature is set to a higher temperature whenever possible within a range where the skeleton of the insulating film is not affected.

50

(3) Third Example

55

[0054] In the third example, investigation is conducted particularly as to how the difference of the amount of methyl groups in the film affects the pore size and the relative dielectric constant. For this purpose, using deposition gas different from that of the first and second examples, a silicon oxide film as the low dielectric constant insulating film is formed on the silicon substrate in the same manner as the first example, and thus a sample is obtained. The silicon oxide film is formed by a plasma enhanced CVD method under the following film-forming conditions.

(Film-forming conditions III)

[0055]

5       (i) Deposition conditions  
HMDSO gas flow rate: 50 SCCM  
H<sub>2</sub>O gas flow rate: 1000 SCCM  
C<sub>2</sub>H<sub>4</sub> gas flow rate: 50 SCCM  
Gas pressure: 1.75 Torr  
10      (ii) Conditions for generating plasma  
High-frequency power (13.56 MHz) PHF: 300W  
Low-frequency power (380 KHz) PLF: 0W  
(iii) Substrate heating temperature: 400°C  
(iv) Deposited silicon oxide film  
15      Film thickness: 650nm

(Ultraviolet processing conditions)

[0056]

20      (i) Ultraviolet light source: Deuterium lamp  
Ultraviolet ray wavelength: 120nm to 400nm  
Power: 30W  
(ii) Substrate-heating temperature: 400°C  
25      (iii) Processing time: 30 minutes

[0057] Regarding the silicon oxide film, investigation is performed for the changes of the relative dielectric constant (k) of the film before and after the ultraviolet irradiation processing.

[0058] According to the investigation results, the relative dielectric constant, which is approximately 2.66 before the ultraviolet irradiation, reduces to 2.45 after the ultraviolet irradiation. As the reason why the reduction ratio of the relative dielectric constant is large in this example, the following is considered. That is, since the source gas contains C<sub>2</sub>H<sub>4</sub> gas, the concentration of methyl groups in the formed film is high. Thus it results in generation of a larger amount of pores. In other words, as an insulating film has larger content of weak bond groups in the state before the ultraviolet irradiation processing, so the reduction effect of relative dielectric constant is larger therein.

35      (4) Fourth Example

[0059] In the fourth example, investigation is conducted for a silicon oxide film deposited by a spin coating method. Using organic SOG containing siloxane bond and Si-CH<sub>3</sub> bond, the silicon oxide film as the low dielectric constant insulating film is formed on the silicon substrate by the spin coating method in the same manner as the first example, and thus a sample is obtained. The silicon oxide film is formed under the following film forming conditions.

(Film forming conditions IV)

45      [0060]

50      (i) Coating conditions  
Coating solution: Alkylsilsesquioxane polymer (MSQ)  
Rotation speed: 2000rpm to 3000rpm  
(ii) Thermal treatment conditions after coating  
Substrate-heating temperature: 400°C  
(iii) Coated silicon oxide film  
Film thickness: 400nm

(Ultraviolet processing conditions)

## [0061]

5       (i) Ultraviolet light source: Deuterium lamp  
           Ultraviolet ray wavelength: 120nm to 400nm  
           Power: 30W  
      (ii) Substrate-heating temperature: 400°C  
      (iii) Processing time: 30 minutes

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[0062] Regarding the silicon oxide film, Fig. 7 shows the investigation result of the pore size distribution before and after the ultraviolet irradiation processing. The axis of ordinate of Fig. 7 shows the strength (arbitrary unit) expressed in linear scale and the axis of abscissa of Fig. 7 shows the pore size (nm).

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[0063] According to Fig. 7, the average pore size, which is 0.81nm before the ultraviolet irradiation, becomes 1.11nm after the ultraviolet irradiation. It is confirmed that the pore size becomes larger in the coated silicon oxide film formed by the spin coating method using MSQ as well. The coated silicon oxide film also has a structure where methyl groups are combined with a part of the silica network structure (skeleton) of Si-O-Si. Therefore, it is considered that the methyl groups are eliminated by the ultraviolet irradiation without affecting the skeleton and thus the pore size becomes larger.

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[0064] As described above, according to the embodiment of the present invention, the insulating film containing Si-CH<sub>3</sub> bond, which is the insulating film originally having a solid skeleton called Si-O-Si, is formed by the plasma enhanced CVD method or the spin coating method, and then the insulating film is subject to not oxidation but the ultraviolet irradiation in the reduced-pressure atmosphere to break CH<sub>3</sub> groups from Si-CH<sub>3</sub> bond in the insulating film, and then they are ejected from the insulating film.

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[0065] In this case, by setting the energy of the irradiated ultraviolet ray higher than the binding energy of Si-CH<sub>3</sub> bond groups and lower than the binding energy of Si-O-Si that forms the skeleton, CH<sub>3</sub> groups can be broken from Si-CH<sub>3</sub> bond in the insulating film without affecting the skeleton of the insulating film.

[0066] This makes it possible to maintain or improve the strength of the insulating film and reduce the dielectric constant of the insulating film.

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[0067] As described above, the present invention has been explained in detail according to the embodiments, but the scope of the invention is not limited to the examples specifically shown in the above-described embodiments, and modifications of the above-described embodiments without departing from the gist of the invention is included in the range of the present invention.

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[0068] For example, the film-forming conditions for the insulating film formed first are described in the first to fourth examples, but the conditions are not limited to them. Film-forming conditions such as being capable of forming an insulating film having Si-CH<sub>n</sub> (n=1, 2, 3) bond to the skeleton of Si-O-Si are acceptable. It is possible to obtain the effect of this invention with the application of this invention as long as the insulating film is one formed under the condition.

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[0069] Furthermore, the following film-forming conditions are acceptable. They are those such as being capable of forming an insulating film containing Si-CH<sub>n</sub> (n=1, 2, 3) bond in the silica skeleton of Si-O other than Si-O-Si, and the insulating film capable of being transformed into a film having solid mechanical strength by the ultraviolet irradiation in the reduced-pressure atmosphere. For example, it is possible to use another organic compound having Si-CH<sub>3</sub> bond, which is dimethyldimethoxysilane (Si(CH<sub>3</sub>)<sub>2</sub>OCH<sub>3</sub>)<sub>2</sub>, tetramethylsilane (Si(CH<sub>3</sub>)<sub>4</sub>), trimethylsilane (SiH(CH<sub>3</sub>)<sub>3</sub>), dimethylsilane (SiH<sub>2</sub>(CH<sub>3</sub>)<sub>2</sub>) or monomethylsilane (SiH<sub>3</sub>(CH<sub>3</sub>)). In this case, the insulating film immediately after deposition has the silica skeleton called Si-O other than Si-O-Si. Although the film strength is not as solid as that of Si-O-Si, the mechanical strength of the film can be improved by the ultraviolet irradiation in the reduced-pressure atmosphere for making porous.

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[0070] As described above, according to the present invention, an initial film-formation is performed for the insulating film containing Si-CH<sub>n</sub> (n=1, 2, 3) bond, which is the insulating film originally having the solid skeleton called Si-O-Si, or the insulating film having the silica skeleton other than Si-O-Si, which is transformed into the insulating film having solid skeleton by the ultraviolet irradiation in the reduced-pressure atmosphere, and then the insulating film is subject to not oxidation but the ultraviolet irradiation in the reduced-pressure atmosphere to break CH<sub>n</sub> groups from Si-CH<sub>n</sub> bond in the insulating film, and then they are ejected from the insulating film.

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[0071] In this case, by setting the energy of the irradiated ultraviolet ray higher than the binding energy of Si-CH<sub>n</sub> bond groups and lower than the binding energy of Si-O-Si or another Si-O, which forms the skeleton, CH<sub>n</sub> groups can be broken from Si-CH<sub>n</sub> bond in the insulating film without affecting the skeleton of the insulating film.

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[0072] This makes it possible to maintain or improve the strength of the insulating film and reduce the dielectric constant of the insulating film.

**Claims**

1. A method of forming a low dielectric constant insulating film, comprising the steps of:

5 (a) forming an insulating film, which contains Si-CH<sub>n</sub> (n=1, 2, 3) bond in Si-O-Si or another silica skeleton, on a substrate;

(b) irradiating ultraviolet ray to said insulating film in reduced-pressure atmosphere or reduced-pressure atmosphere mainly containing inert gas and nitrogen to break CH<sub>n</sub> groups from Si-CH<sub>n</sub> bond in said insulating film; and

10 (c) ejecting said broken CH<sub>n</sub> groups from said insulating film.

2. The method of forming the low dielectric constant insulating film according to claim 1, wherein the wavelength of said ultraviolet ray is 120nm or more.

15 3. The method of forming the low dielectric constant insulating film according to claim 1, wherein the step of said (c) is performed after the step of said (b).

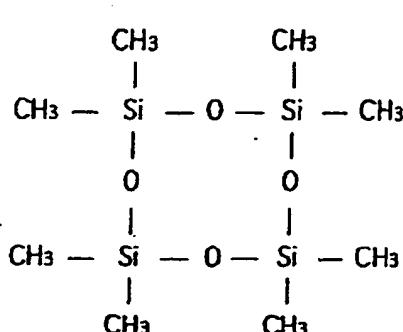
4. The method of forming the low dielectric constant insulating film according to claim 3, wherein the step of said (b) and the step of said (c) are performed simultaneously by irradiating ultraviolet ray on said 20 insulating film in the reduced-pressure atmosphere while heating said substrate.

5. The method of forming the low dielectric constant insulating film according to claim 1, wherein the steps of said (a) to said (c) are performed repeatedly.

25 6. The method of forming the low dielectric constant insulating film according to claim 1, wherein the step of said (a) is a step of applying electric power to deposition gas, which contains an organic compound having Si-CH<sub>3</sub> bond and siloxane bond or an another organic compound having Si-CH<sub>3</sub> bond and oxidizing gas, to generate plasma so that reaction is caused to form a CVD insulating film containing said Si-CH<sub>n</sub> bond on said substrate.

30 7. The method of forming the low dielectric constant insulating film according to claim 6, wherein the organic compound having said Si-CH<sub>3</sub> bond and siloxane bond is any one of hexamethyldisiloxane (HMDSO: (CH<sub>3</sub>)<sub>3</sub>Si-O-Si(CH<sub>3</sub>)<sub>3</sub>), octamethylcyclotetrasiloxane (OMCTS),

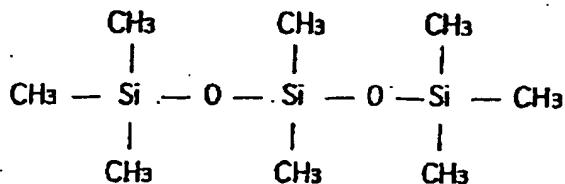
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**[formula 4]**

octamethyltrisiloxane (OMTS),

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## [formula 5]

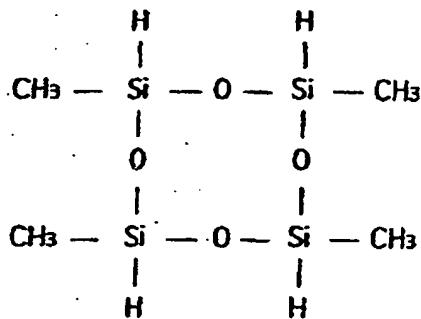


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and  
tetramethylcyclotetrasiloxane (TMCTS)

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## [formula 6]



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30 8. The method of forming the low dielectric constant insulating film according to claim 6, wherein another organic compound having said Si-CH<sub>3</sub> bond is any one of dimethyldimethoxysilane (Si(CH<sub>3</sub>)<sub>2</sub>OCH<sub>3</sub>)<sub>2</sub>, tetramethylsilane (Si(CH<sub>3</sub>)<sub>4</sub>), trimethylsilane (SiH(CH<sub>3</sub>)<sub>3</sub>), dimethylsilane (SiH<sub>2</sub>(CH<sub>3</sub>)<sub>2</sub>) and monomethylsilane (SiH<sub>3</sub>(CH<sub>3</sub>)).

35 9. The method of forming the low dielectric constant insulating film according to claim 6, wherein said oxidizing gas is at least one of H<sub>2</sub>O, N<sub>2</sub>O, CH<sub>3</sub>OH, C<sub>2</sub>H<sub>5</sub>OH, H<sub>2</sub>O<sub>2</sub> and O<sub>2</sub>.

40 10. The method of forming the low dielectric constant insulating film according to claim 6, wherein said deposition gas contains C<sub>x</sub>H<sub>y</sub>F<sub>z</sub> (x is a positive integer, y is 0 or a positive integer, z is 0 or a positive integer, and y and z do not become 0 at the same time) in addition to the organic compound having siloxane bond and the oxidizing gas.

45 11. The method of forming the low dielectric constant insulating film according to claim 10, wherein said C<sub>x</sub>H<sub>y</sub>F<sub>z</sub> is C<sub>4</sub>F<sub>8</sub> and C<sub>2</sub>F<sub>4</sub>.

50 12. The method of forming the low dielectric constant insulating film according to Claim 1, wherein the step of said (a) is a step of coating organic SOG having Si-CH<sub>3</sub> bond and siloxane bond on said substrate to form a coated insulating film containing Si-CH<sub>n</sub> (n=1, 2, 3) bond.

55 13. The method of forming the low dielectric constant insulating film according to claim 12, wherein said organic SOG having siloxane bond is alkylsilsesquioxane polymer (MSQ).

14. A low dielectric constant insulating film characterized in having a low dielectric constant and sufficient mechanical strength on the basis of an initial insulating film being non-porous or porous and having Si-CH<sub>3</sub> bond with respect

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to the silica skeleton of Si-O-Si, or Si-O other than Si-O-Si, wherein CH<sub>3</sub> group in said initial insulating film is broken by ultraviolet irradiation in reduced-pressure atmosphere and said CH<sub>3</sub> group is ejected from said initial insulating film by heat treatment.

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FIG. 1

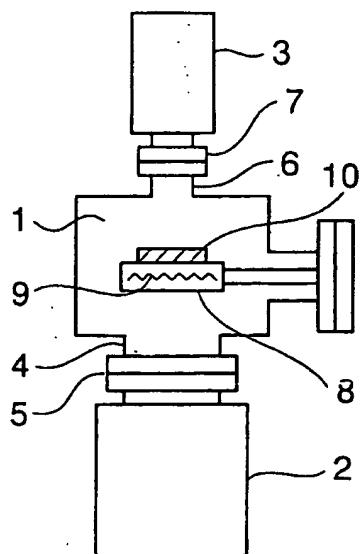


FIG. 2

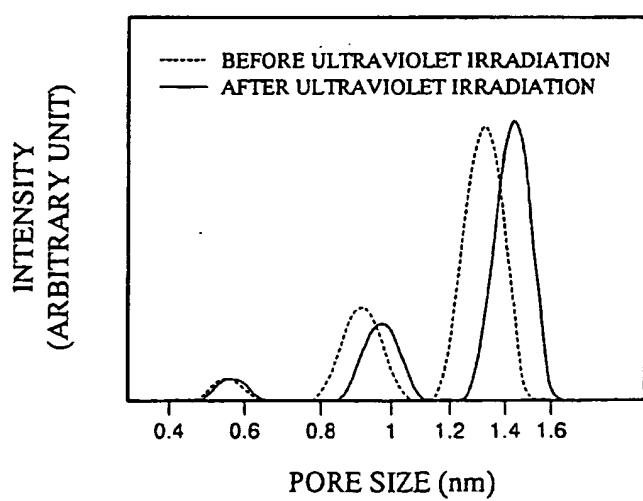


FIG. 3

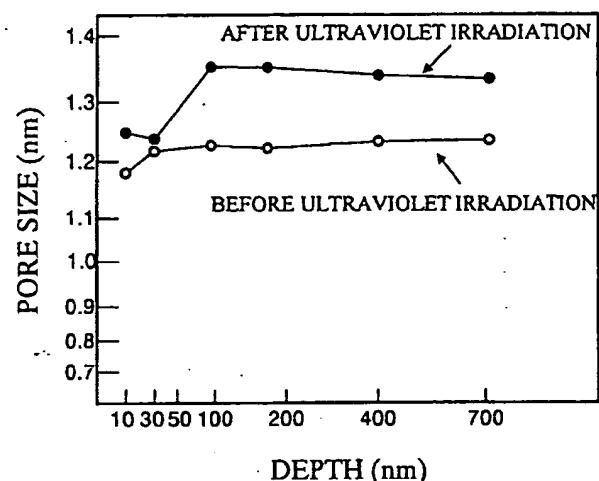


FIG. 4

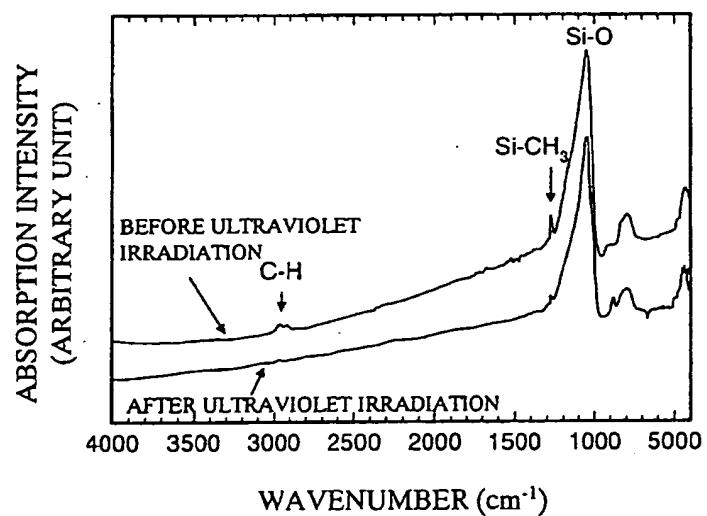


FIG. 5

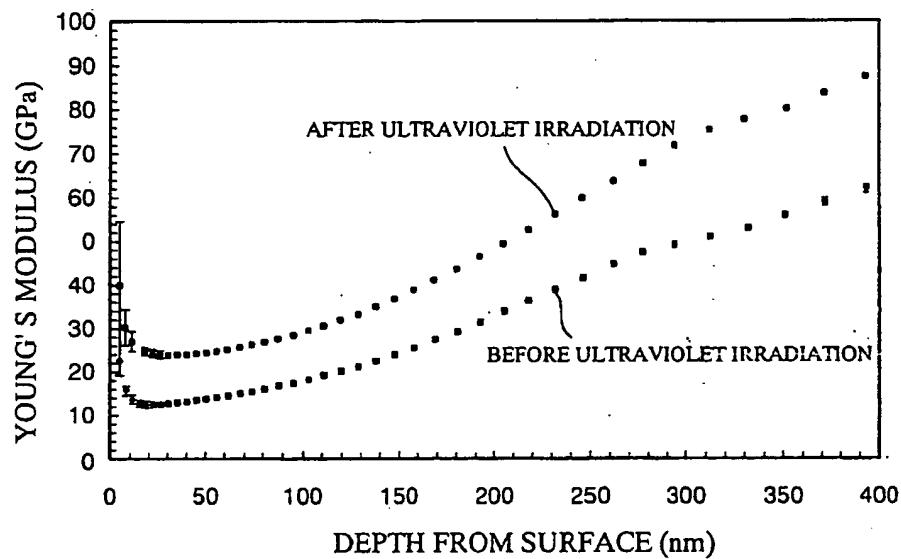


FIG. 6

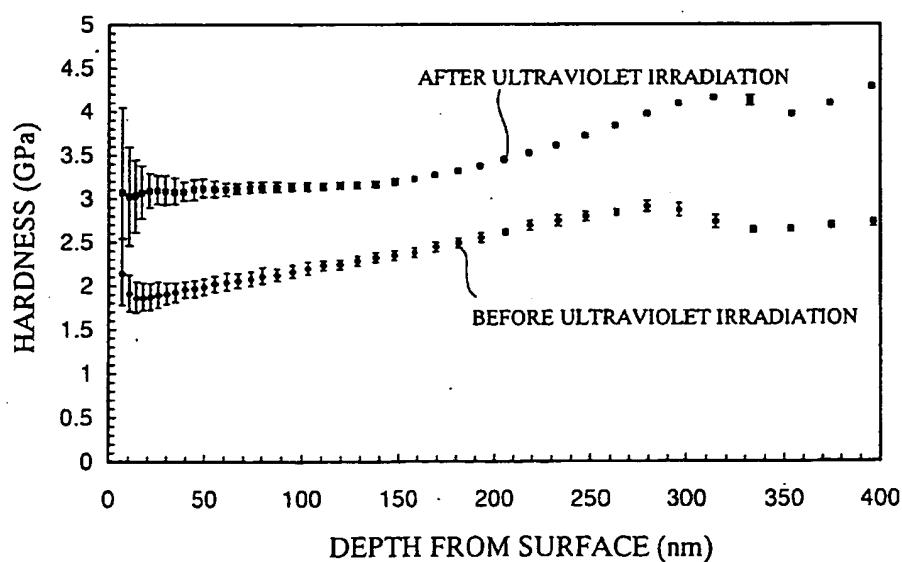


FIG. 7

